

CLAIMS:

1. A data carrier (2) for the communication of communication data (KD1, KD2) with a base station, having

- processing means (4) for the processing of communicated communication data (KD1, KD2), and having

5 - voltage supply means (5) which are arranged to receive an external supply voltage (U_{EXT}) applied to the data carrier during a charging time interval (T_L) until a turn-on instant (t_{e1} , t_{e2} , t_{e3}) and which are adapted to supply an internal supply voltage (U_{INT}) to the processing means (4), decoupled from the external supply voltage (U_{EXT}), during a consumption time interval (T_{V1} , T_{V2} , T_{V3}) starting at the turn-on instant (t_{e1} , t_{e2} , t_{e3}), the processing means (4) being adapted to interrupt the processing from an interruption instant (t_{u1} , t_{u2} , t_{u3}), when the internal supply voltage (U_{INT}) decreases below a threshold voltage (U_S), till the turn-on instant (t_{e1} , t_{e2} , t_{e3}),

10 characterized in that there are provided time measurement means (12) which are adapted to measure a processing time interval (T_{P1} , T_{P2} , T_{P3}) defined as the time interval from the turn-on instant (t_{e1} , t_{e2} , t_{e3}) till the interruption instant (t_{u1} , t_{u2} , t_{u3}), and the voltage supply means (5) are adapted to adapt the consumption time interval (T_{V1} , T_{V2} , T_{V3}) to the measured processing time interval (T_{P1} , T_{P2} , T_{P3}).

2. A data carrier (2) as claimed in claim 1, characterized in that the voltage

20 supply means (5) are adapted to reduce the consumption time interval (T_{V1} , T_{V2} , T_{V3}) stepwise when the consumption time interval (T_{V1} , T_{V2} , T_{V3}) is longer than the processing time interval (T_{P1} , T_{P2} , T_{P3}).

3. A data carrier (2) as claimed in claim 1, characterized in that the voltage

25 supply means (5) are adapted to prolong the consumption time interval (T_{V1} , T_{V2} , T_{V3}) to a nominal consumption time interval when the internal supply voltage (U_{INT}) does not decrease below the threshold voltage (U_S) during the consumption time interval (T_{V1} , T_{V2} , T_{V3}).

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4. A data carrier (2) as claimed in claim 1, characterized in that the voltage supply means (5) are adapted to prolong the consumption time interval (T_{V1} , T_{V2} , T_{V3}) to a random consumption time interval selected at random from a plurality of possible nominal consumption time intervals when the internal supply voltage (U_{INT}) does not decrease below the threshold voltage (U_S) during the consumption time interval (T_{V1} , T_{V2} , T_{V3}).

5. A data carrier (2) as claimed in claim 1, characterized in that memory means are adapted to store power information characteristic of the power consumption of the processing means (4) during the execution of processing steps of the processing program, and the voltage supply means (5) are adapted to define the consumption time interval (T_{V1} , T_{V2} , T_{V3}) in accordance with the power information stored for the next processing steps to be executed.

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